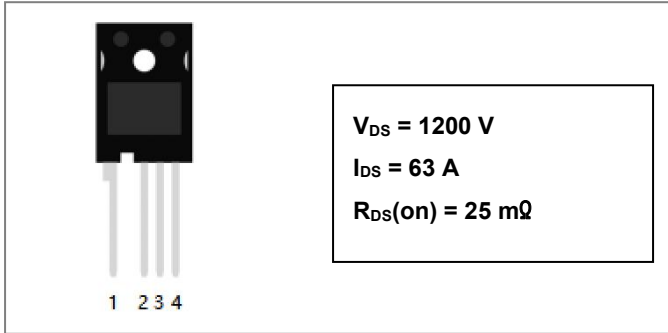


# S2M0025120K

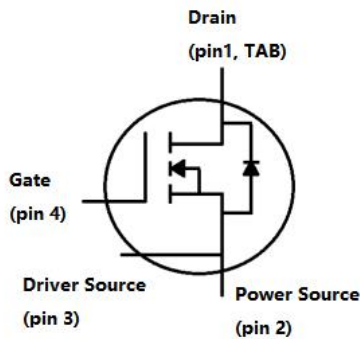
## 1200V SiC POWER MOSFET



### Description

S2M0025120K is single SiC Power MOSFET packaged in TO-247-4 case. The device is a high voltage n-channel enhancement mode MOSFET that has very low total conduction losses and very stable switching characteristics over temperature extremes. The S2M0025120K is ideal for energy sensitive, high frequency applications in challenging environments.

### Circuit Diagram



### Features

- Positive temperature characteristics, easy to parallel.
- Low on-resistance Typ. RDS(on) = 25mΩ .
- Fast switching speed and low switching losses.
- Very fast and robust intrinsic body diode.
- Process of non-bright Tin electroplatin

### Applications

- EV Fast Charging Modules
- EV On Board Chargers
- Solar Inverters
- Online UPS/Industrial UPS
- SMPS (Switch Mode Power Supplies)
- DC-DC Converters
- ESS (Energy Storage Systems)

### Maximum Ratings(T=25°C unless otherwise specified)

Characteristics	Symbol	Condition	Max.	Units
Drain Source Voltage	V <sub>DSS</sub>	V <sub>GS</sub> = 0V, I <sub>DS</sub> = 100uA, T <sub>C</sub> = 25°C	1200	V
Gate Source Voltage	V <sub>GSS</sub>	T <sub>C</sub> = 25°C, Absolute maximum values, AC (f>1Hz)	-10 to +25	V
Gate Source Voltage	V <sub>GSOP</sub>	T <sub>C</sub> = 25°C Recommended Operational Values	-5 to +20	V
Continuous Drain Current	I <sub>D</sub>	V <sub>GS</sub> = 20V, T <sub>C</sub> = 25°C	63	A
	I <sub>D</sub>	V <sub>GS</sub> = 20V, T <sub>C</sub> = 100°C	39	A
Pulsed Drain Current	I <sub>D,pulse</sub>	Pulse width t <sub>p</sub> limited by T <sub>Jmax</sub>	250	A
Power Dissipation	P <sub>D</sub>	T <sub>C</sub> =25°C, T <sub>J</sub> = 175 °C	517	W
Solder Temperature	TL	1.6mm (0.063") from case for 10s	260	°C

**Electrical Characteristics(T=25°C unless otherwise specified)**

Characteristics	Symbol	Condition	Min.	Typ.	Max.	Units
Drain Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 100\mu A$	1200			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 15mA$	1.8	2.1	4	V
		$V_{DS} = V_{GS}, I_D = 15mA, T_J = 150^\circ C$		1.4		V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 1200V, V_{GS} = 0V$		2	100	$\mu A$
Gate Source Leakage Current	$I_{GSS}$	$V_{GS} = 20V, V_{DS} = 0V$			250	nA
Drain Source On-State Resistance	$R_{DS(on)}$	$V_{GS} = 20V, I_D = 50A$		25	34	m $\Omega$
		$V_{GS} = 20V, I_D = 50A, T_J = 150^\circ C$		41		m $\Omega$
Transconductance	gfs	$V_{DS} = 20V, I_{DS} = 50A$		13		S
		$V_{DS} = 20V, I_{DS} = 50A, T_J = 150^\circ C$		14		S
Input Capacitance	$C_{ISS}$	$V_{GS} = 0V,$		4402		pF
Output Capacitance	$C_{OSS}$	$V_{DS} = 1000V$		257		
Reverse Transfer Capacitance	$C_{RSS}$	$V_{AC} = 25mV$		7		
$C_{OSS}$ Stored Energy	$E_{OSS}$	$f = 1MHz$		128		$\mu J$
Turn-On Switching Energy	$E_{ON}$	$V_{DS} = 800V, V_{GS} = -5/20V$		0.61		mJ
Turn-Off Switching Energy	$E_{OFF}$	$I_D = 50A, R_{G(ext)} = 2.5\Omega$		0.31		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS} = 800V, V_{GS} = -5/20V$		20		ns
Rise Time	$t_r$	$I_D = 50A, R_{G(ext)} = 2.5\Omega$		24		
Turn-Off Delay Time	$t_{d(off)}$			36		
Fall Time	$t_f$			18		
Internal Gate Resistance	$R_{G(int)}$	$f = 1MHz, V_{AC} = 25mV$		2.5		$\Omega$
Gate to Source Charge	$Q_{gs}$	$V_{DS} = 800V, V_{GS} = -5/20V$		37		nC
Gate to Drain Charge	$Q_{gd}$	$I_D = 50A$		38		
Total Gate Charge	$Q_g$			130		

**Reverse Diode Characteristics:**

Characteristics	Symbol	Condition	Typ.	Max.	Units
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> = -5V, I <sub>SD</sub> = 25A	4.1		V
		V <sub>GS</sub> = -5V, I <sub>SD</sub> = 25A, T <sub>J</sub> = 150°C	3.6		V
Continuous Diode Forward Current	I <sub>S</sub>	V <sub>GS</sub> = -5V, T <sub>C</sub> = 25°C		63	A
Reverse Recovery Time	t <sub>rr</sub>	V <sub>GS</sub> = -5V, I <sub>SD</sub> = 50A, T <sub>J</sub> = 25°C V <sub>R</sub> = 800V dif/dt= 1057A/μs	48		ns
Reverse Recovery Charge	Q <sub>rr</sub>		354		nC
Peak Reverse Recovery Current	I <sub>mm</sub>		12		A

**Thermal-Mechanical Specifications:**

Characteristics	Symbol	Condition	Specification	Units
Junction Temperature	T <sub>J</sub>	-	-55 to +175	°C
Storage Temperature	T <sub>stg</sub>	-	-55 to +175	°C
Typical Thermal Resistance Junction to Case	R <sub>θJC</sub>	DC operation	0.28	°C/W
Typical Thermal Resistance Junction to Ambient	R <sub>θJA</sub>		32	°C/W

**Ordering Information:**

Device	Package	Shipping
S2M0025120K	TO-247-4	30pcs/tube

**Marking Diagram**


Where XXXXX is YYWWL

S2M = Device Type  
 0025 = R<sub>DS(on)</sub>  
 120 = Reverse Voltage (1200V)  
 K = Package  
 SSG = SSG  
 YY = Year  
 WW = Week  
 L = Lot Number

**Cautions:** Molding resin  
 Epoxy resin UL:94V-0

**Ratings and Characteristics Curves**

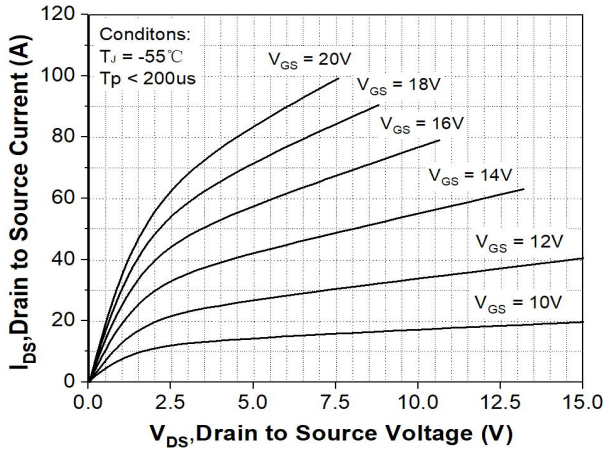


Figure 1. Output Characteristics  $T_J = -55^\circ\text{C}$

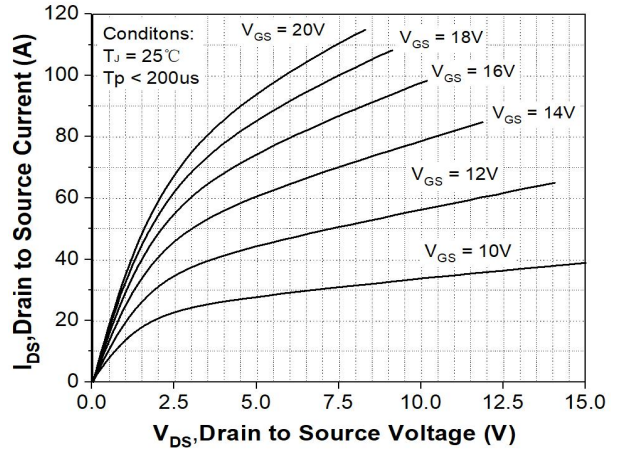


Figure 2. Output Characteristics  $T_J = 25^\circ\text{C}$

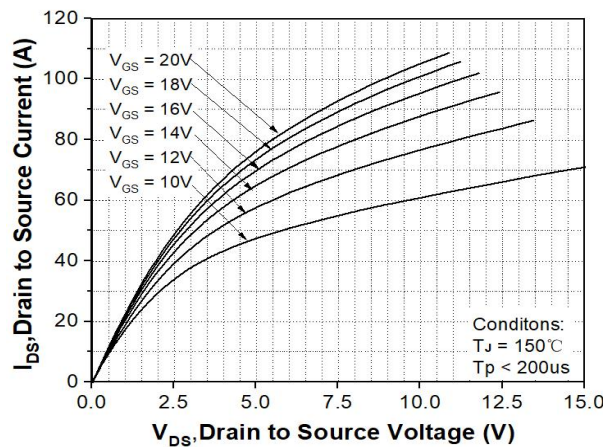


Figure 3. Output Characteristics  $T_J = 150^\circ\text{C}$

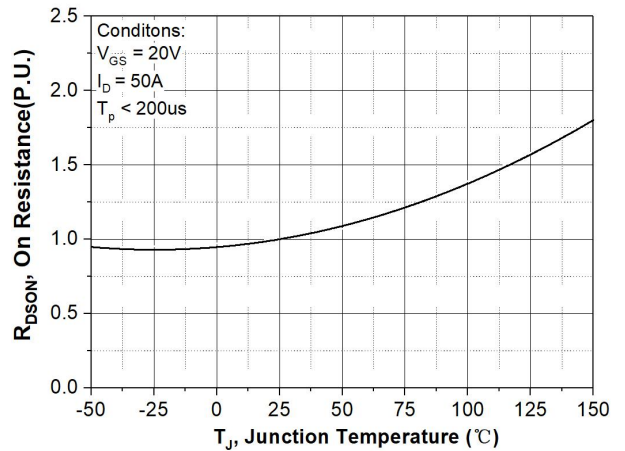


Figure 4. Normalized On-Resistance vs. Temperature

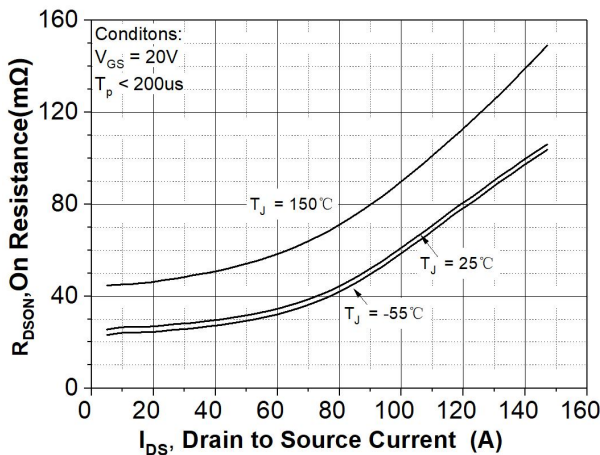


Figure 5. On-Resistance vs. Drain Current For Various Temperatures

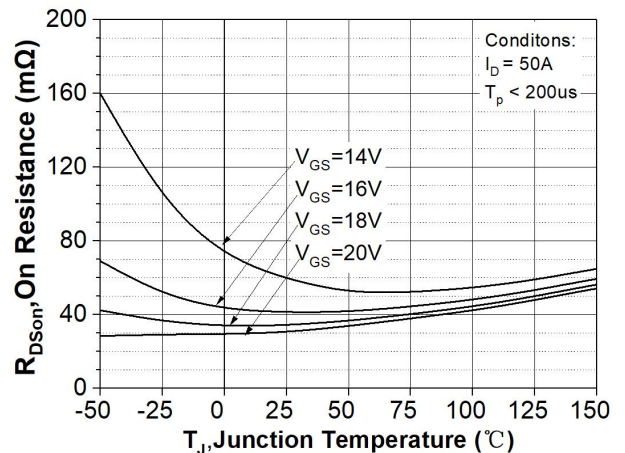


Figure 6. On-Resistance vs. Temperature For Various Gate Voltage

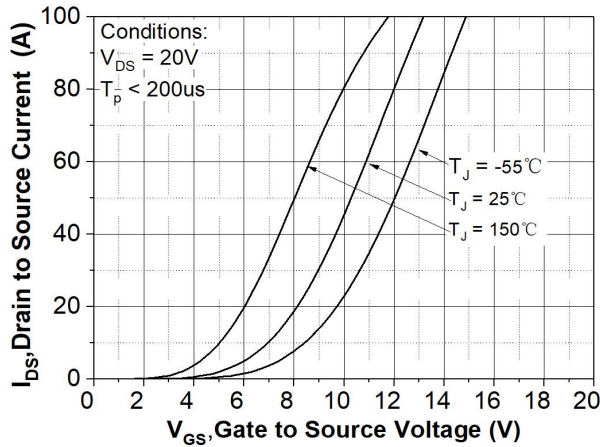


Figure 7. Transfer Characteristic for Various Junction Temperatures

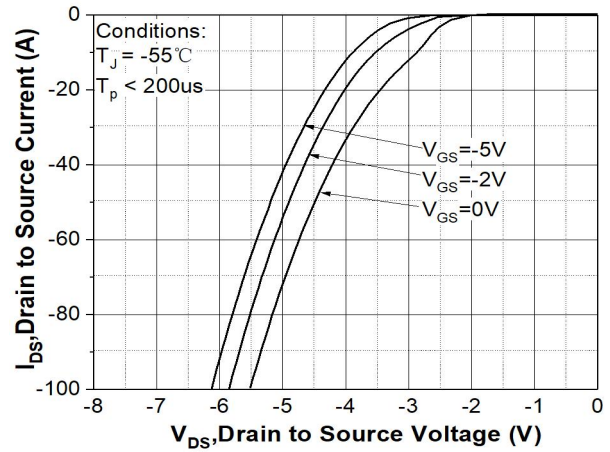


Figure 8. Body Diode Characteristic at  $T_J = -55^\circ C$

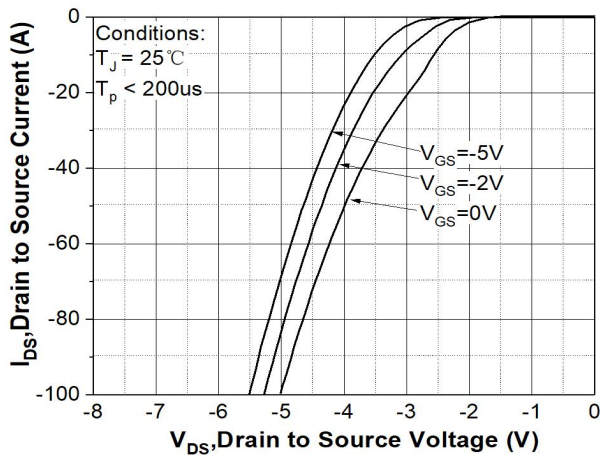


Figure 9. Body Diode Characteristic at  $T_J = 25^\circ C$

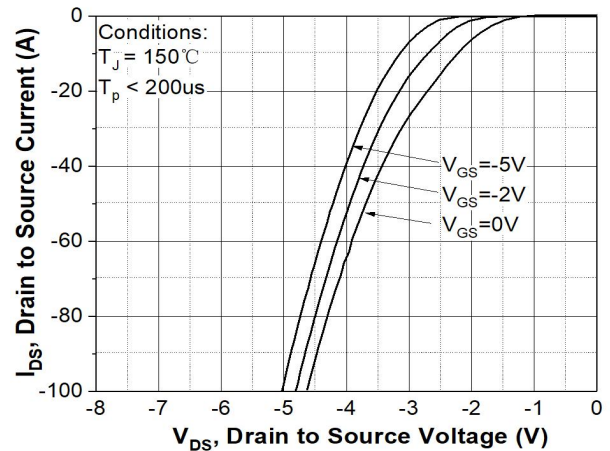


Figure 10. Body Diode Characteristic at  $T_J = 150^\circ C$

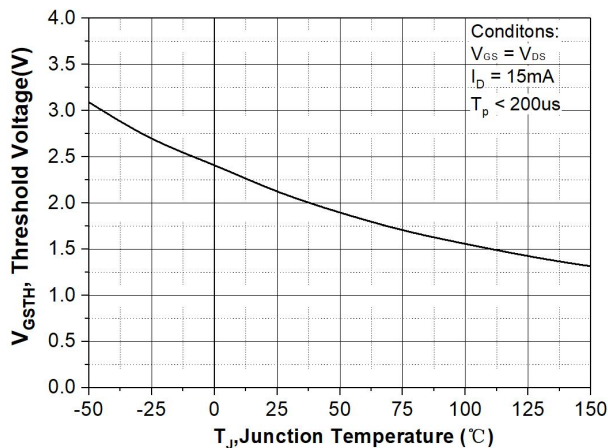


Figure 11. Threshold Voltage vs. Temperature

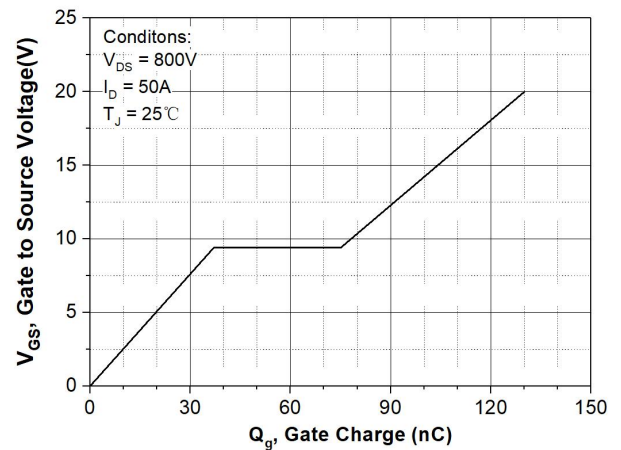


Figure 12. Gate Charge Characteristic



**Technical Data**  
**Data Sheet N2415, REV.A**

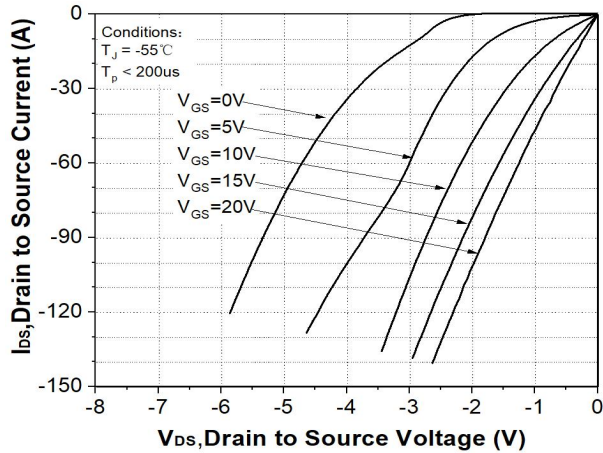


Figure 13. 3rd Quadrant Characteristic at  $T_J = -55^\circ\text{C}$

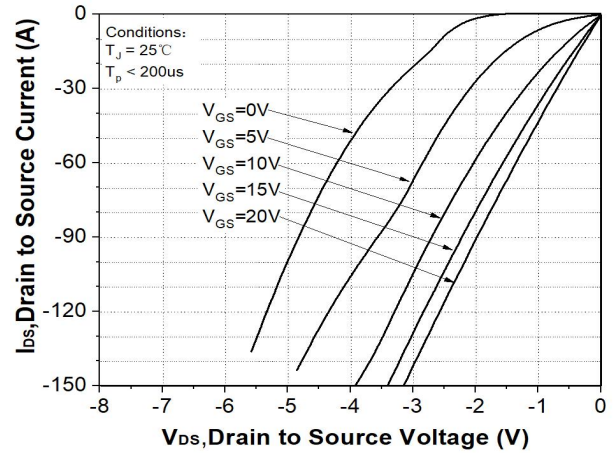


Figure 14. 3rd Quadrant Characteristic at  $T_J = 25^\circ\text{C}$

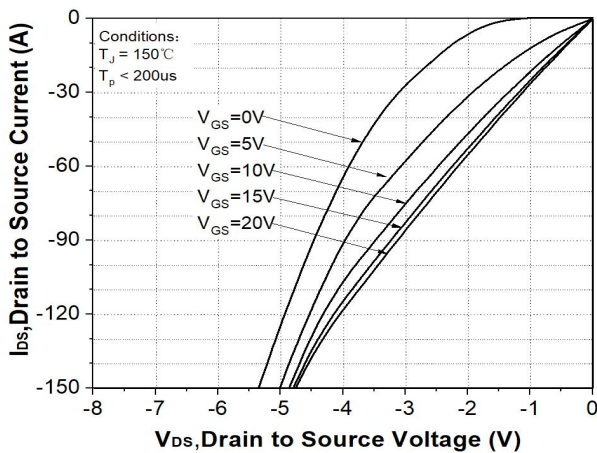


Figure 15. 3rd Quadrant Characteristic at  $T_J = 150^\circ\text{C}$

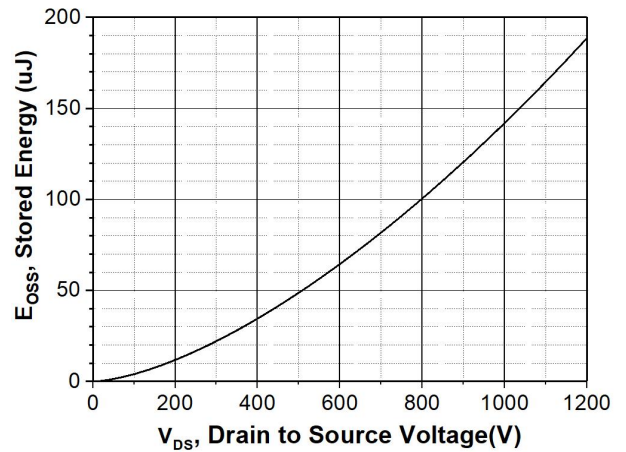


Figure 16. Output Capacitor Stored Energy

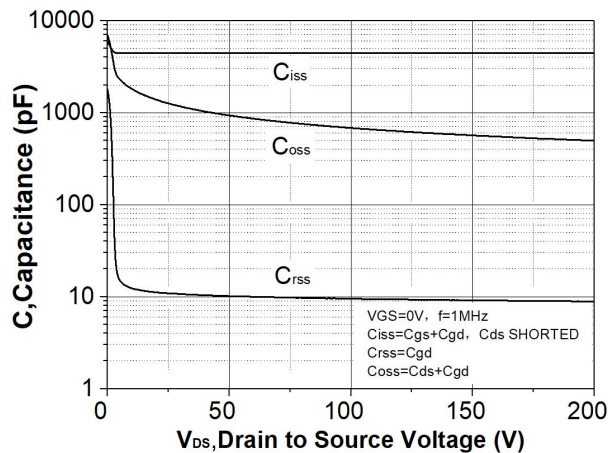


Figure 17. Capacitances vs. Drain-Source Voltage (0 - 200V)

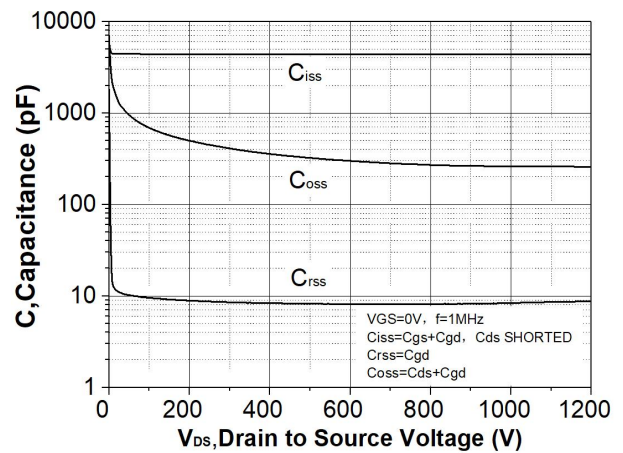


Figure 18. Capacitances vs. Drain-Source Voltage (0 - 1000V)

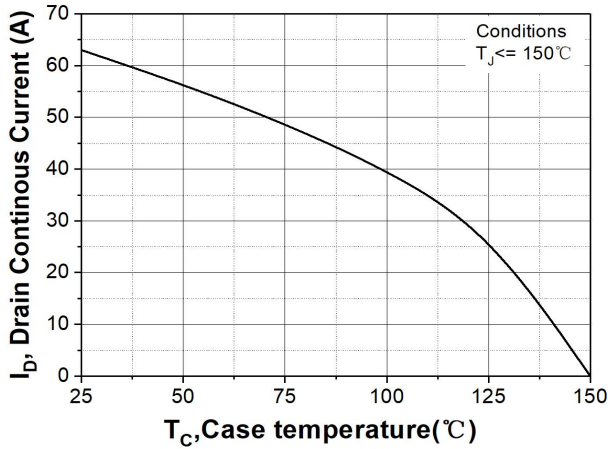


Figure 19. Continuous Drain Current Derating vs. Case Temperature

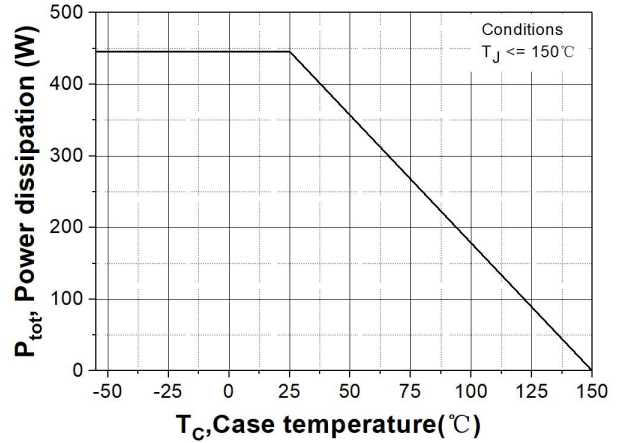


Figure 20. Maximum Power Dissipation Derating vs. Case Temperature

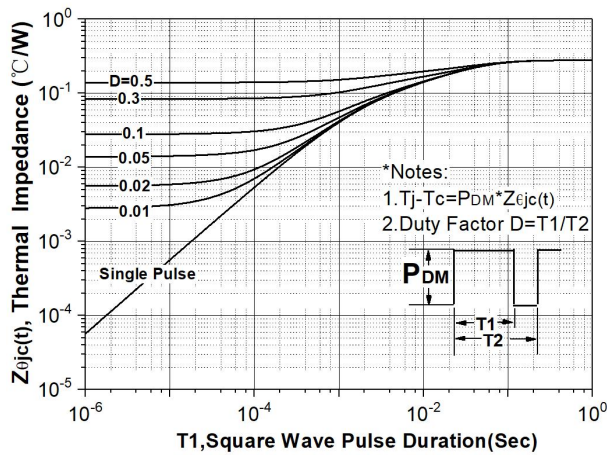


Figure 21. Transient Thermal Impedance (Junction - Case)

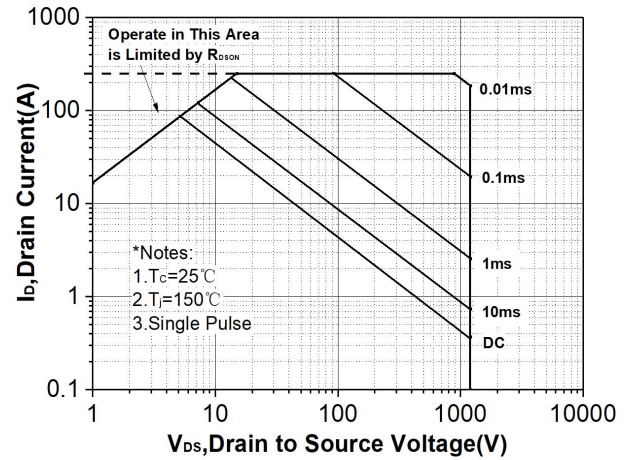


Figure 22. Safe Operating Area

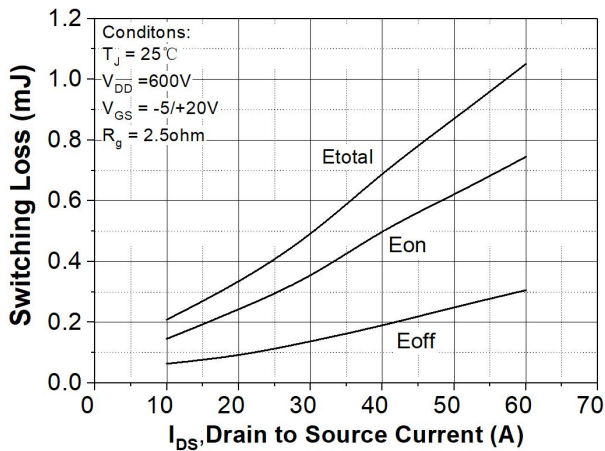


Figure 23. Clamped Inductive Switching Energy vs. Drain Current ( $V_{DD} = 600V$ )

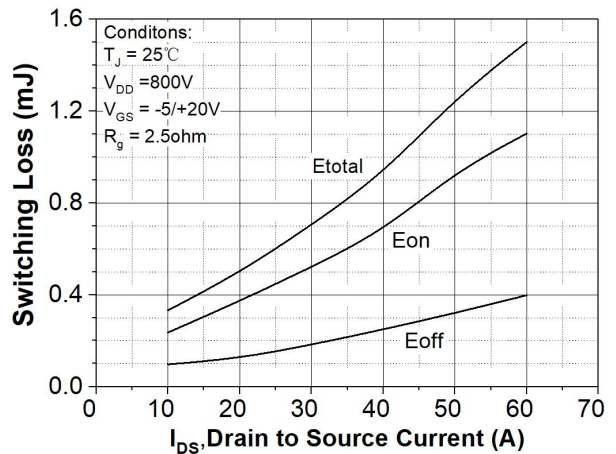


Figure 24. Clamped Inductive Switching Energy vs. Drain Current ( $V_{DD} = 800V$ )

**Technical Data**  
Data Sheet N2415, REV.A

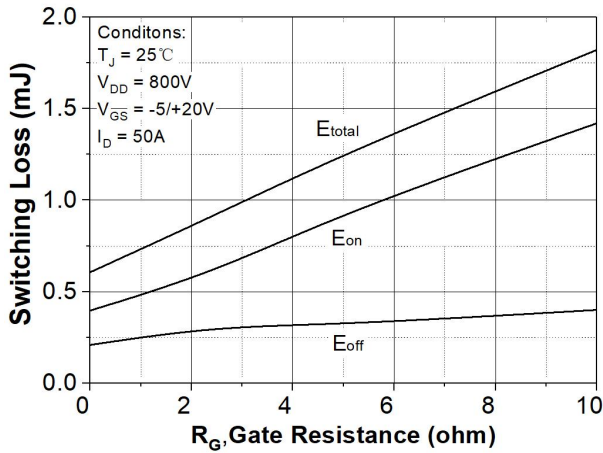


Figure 25. Clamped Inductive Switching Energy vs.  $R_{G(ext)}$

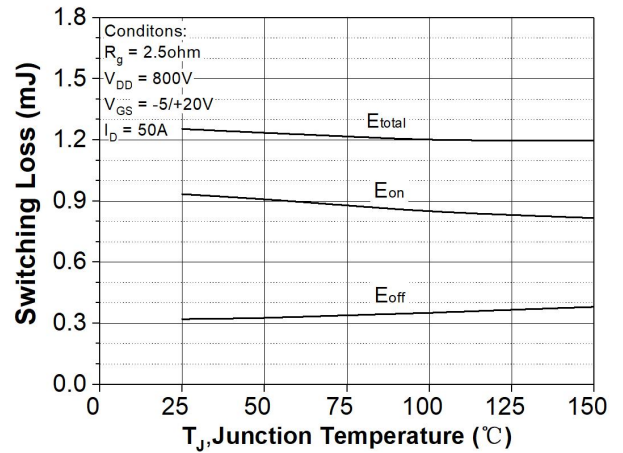


Figure 26. Clamped Inductive Switching Energy vs. Temperature

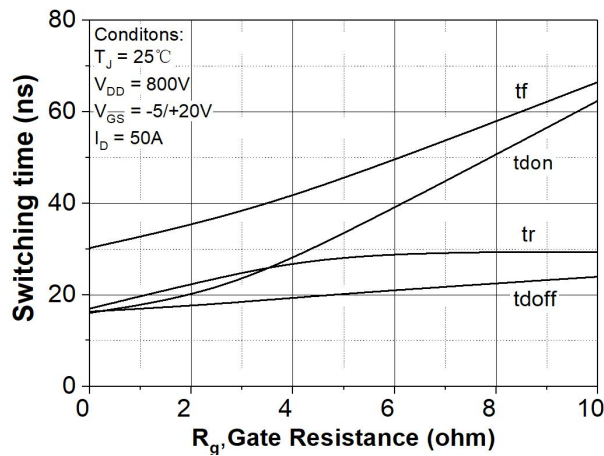


Figure 27. Switching Times vs.  $R_{G(ext)}$

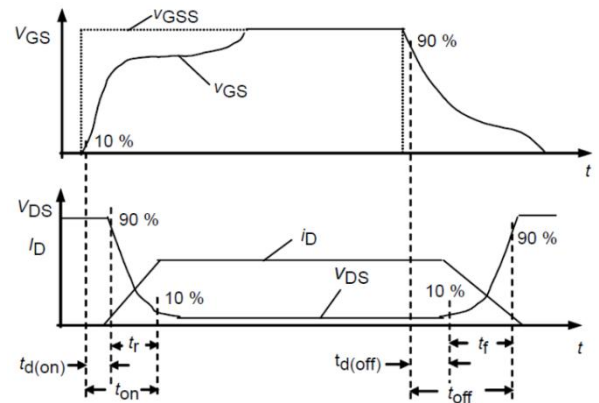


Figure 28. Switching Times Definition

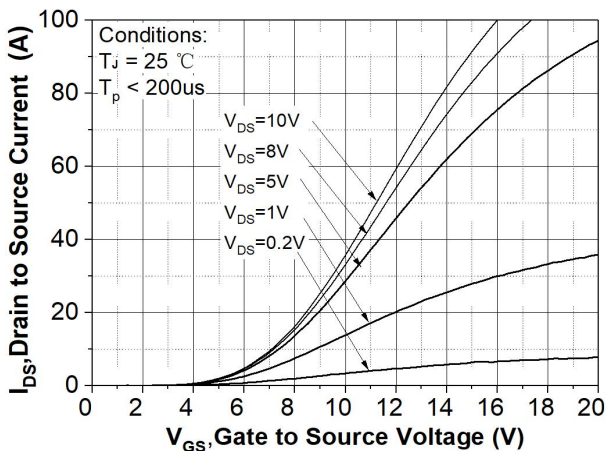
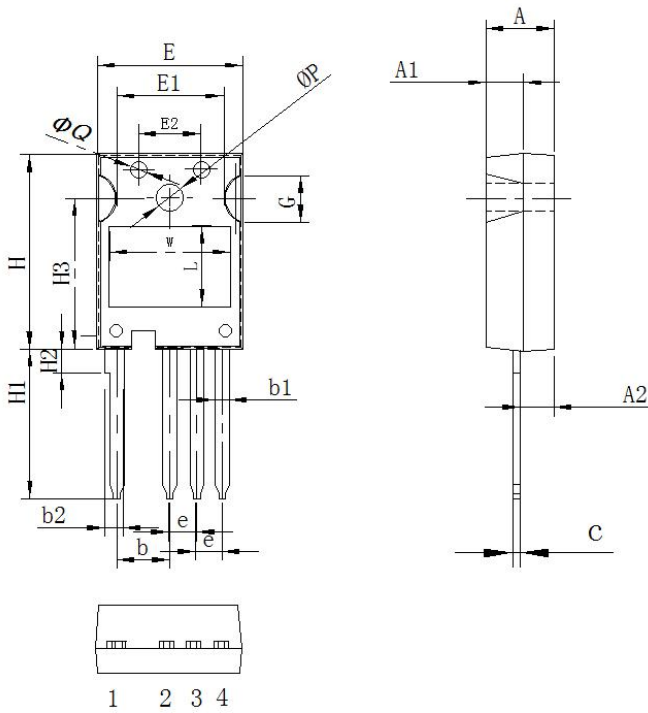


Figure 29. Transfer Characteristic for Various Drain Source Voltage



**Mechanical Dimensions TO-247-4**



Symbol	In mm		
	Min	Nom	Max
A	4.80	5.00	5.21
A1	2.29	3.00	3.20
A2	1.91	2.40	2.60
b	4.85	5.05	5.25
b1	1.05	1.25	1.60
b2	1.07	2.30	2.50
c	0.50	0.60	0.70
e	2.35	2.55	2.75
E	15.50	15.70	16.13
E1	10.50	10.70	10.90
E2	6.35	7.60	7.80
G	4.80	5.00	5.20
H	22.40	22.60	23.60
H1	17.31	18.50	18.70
H2	2.50	3.00	4.37
H3	16.00	16.50	17.00
$\Phi P$	3.00	3.60	3.80
$\Phi Q$	2.20	2.50	3.00

**DISCLAIMER:**

- 1- The information given herein, including the specifications and dimensions, is subject to change without prior notice to improve product characteristics. Before ordering, purchasers are advised to contact the SMC Diode Solutions sales department for the latest version of the datasheet(s).
- 2- In cases where extremely high reliability is required (such as use in nuclear power control, aerospace and aviation, traffic equipment, medical equipment, and safety equipment), safety should be ensured by using semiconductor devices that feature assured safety or by means of users' fail-safe precautions or other arrangement.
- 3- In no event shall SMC Diode Solutions be liable for any damages that may result from an accident or any other cause during operation of the user's units according to the datasheet(s). SMC Diode Solution assumes no responsibility for any intellectual property claims or any other problems that may result from applications of information, products or circuits described in the datasheets.
- 4- In no event shall SMC Diode Solutions be liable for any failure in a semiconductor device or any secondary damage resulting from use at a value exceeding the absolute maximum rating.
- 5- No license is granted by the datasheet(s) under any patents or other rights of any third party or SMC Diode Solutions.
- 6- The datasheet(s) may not be reproduced or duplicated, in any form, in whole or part, without the expressed written permission of SMC Diode Solutions.
- 7- The products (technologies) described in the datasheet(s) are not to be provided to any party whose purpose in their application will hinder maintenance of international peace and safety nor are they to be applied to that purpose by their direct purchasers or any third party. When exporting these products (technologies), the necessary procedures are to be taken in accordance with related laws and regulations..